

## FDD306P

# P-Channel 1.8V Specified PowerTrench® MOSFET

### **Features**

- -6.7 A, -12 V.  $R_{DS(ON)} = 28 \text{ m}\Omega @V_{GS} = -4.5 \text{ V}$   $R_{DS(ON)} = 41 \text{ m}\Omega @V_{GS} = -2.5 \text{ V}$  $R_{DS(ON)} = 90 \text{ m}\Omega @V_{GS} = -1.8 \text{ V}$
- Fast switching speed
- $\blacksquare$  High performance trench technology for extremely low  $R_{DS(ON)}$
- High power and current handling capability

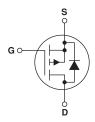
## **Applications**

■ DC/DC converter

### **General Description**

This P-Channel 1.8V Specified MOSFET uses Fairchild's advanced low voltage PowerTrench process. It has been optimized for battery power management.





## Absolute Maximum Ratings T<sub>A</sub>=25°C unless otherwise noted

Symbol	Parameter		Ratings	Units
V <sub>DSS</sub>	Drain-Source Voltage		-12	V
V <sub>GSS</sub>	Gate-Source Voltage		±8	V
I <sub>D</sub>	Drain Current — Continuous (Note 3)		-6.7	Α
	- Pulsed	(Note 1a)	-54	
P <sub>D</sub>	Power Dissipation for Single Operation	(Note 1)	52	W
		(Note 1a)	3.8	
		(Note 1b)	1.6	
$T_J, T_{STG}$	Operating and Storage Junction Temperature Range	−55 to +175	°C	
Thermal Characteristics				
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	(Note 1)	2.9	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	(Note 1a)	40	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient (Note 1b)		96	°C/W

## **Package Marking and Ordering Information**

Device Marking	Device	Reel Size	Tape width	Quantity
FDD306P	FDD306P	13"	12mm	2500 units

# **Electrical Characteristics** $T_A = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Off Charac	cteristics			Į.	ļ	'
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$				V
$\frac{\Delta BV_{DSS}}{\Delta T_J}$	Breakdown Voltage Temperature Coefficient	$I_D = -250 \mu A$ , Referenced to 25°C		-0.6		mV/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}$			-1	μА
I <sub>GSSF</sub>	Gate-Body Leakage	$V_{GS} = \pm 8V$ , $V_{DS} = 0 V$			±100	nA
On Charac	cteristics (Note 2)		•	•		
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = -250 \mu\text{A}$	-0.4	-0.5	-1.5	V
$\frac{\Delta V_{\text{GS(th)}}}{\Delta T_J}$	Gate Threshold Voltage Temperature Coefficient	$I_D = -250 \mu A$ , Referenced to $25^{\circ}C$		2.2		mV/°C
R <sub>DS(on)</sub>	Static Drain–Source On–Resistance	$\begin{aligned} &V_{GS} = -4.5 \text{ V, } I_D = -6.7 \text{ A} \\ &V_{GS} = -2.5 \text{ V, } I_D = -6.1 \text{ A} \\ &V_{GS} = -1.8 \text{ V, } I_D = -4.8 \text{ A} \\ &V_{GS} = -4.5 \text{ V, } I_D = -6.7 \text{ A, } T_J = 125^{\circ}\text{C} \end{aligned}$		21 29 42 25	28 41 90	mΩ
I <sub>D(on)</sub>	On-State Drain Current	$V_{GS} = -4.5 \text{ V}, V_{DS} = -5 \text{ V}$	-45			Α
9 <sub>FS</sub>	Forward Transconductance	$V_{DS} = -5 \text{ V}, I_{D} = -6.7 \text{ A}$		22		S
Dynamic (	Characteristics		•			
C <sub>iss</sub>	Input Capacitance	$V_{DS} = -6 \text{ V}, V_{GS} = 0 \text{ V},$ f = 1.0  MHz		1290		pF
C <sub>oss</sub>	Output Capacitance			590		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			430		pF
R <sub>G</sub>	Gate Resistance	V <sub>GS</sub> = 15 mV, f = 1.0 MHz		4.2		Ω
Switching	Characteristics (Note 2)					
t <sub>d(on)</sub>	Turn-On Delay Time	$V_{DD} = -6 \text{ V}, I_D = -1 \text{ A},$		16	29	ns
t <sub>r</sub>	Turn-On Rise Time	$V_{GS} = -4.5 \text{ V}, R_{GEN} = 6 \Omega$		8	16	ns
t <sub>d(off)</sub>	Turn-Off Delay Time			34	54	ns
t <sub>f</sub>	Turn-Off Fall Time			41	65	ns
Qg	Total Gate Charge	$V_{DS} = -6V$ , $I_{D} = -6.7$ A,		15	21	nC
Q <sub>gs</sub>	Gate-Source Charge	$V_{GS} = -4.5 \text{ V}$		2.0		nC
Q <sub>gd</sub>	Gate-Drain Charge			4.4		nC
Drain-Sou	irce Diode Characteristics and Maximum Ra	atings				
I <sub>S</sub>	Maximum Continuous Drain-Source Diode Fo	orward Current			-3.2	Α
V <sub>SD</sub>	Drain-Source Diode Forward Voltage	$V_{GS} = 0 \text{ V}, I_S = -3.2 \text{ A}$ (Note 2)		-0.8	-1.2	V
Trr	Diode Reverse Recovery Time	IF = -6.7 A,		37		ns
Irm	Diode Reverse Recovery Current	diF/dt = 100 A/μs (Note 3)		0.9		Α
Qrr	Diode Reverse Recovery Charge	1		17		nC

Notes:
 1. R<sub>θ,JA</sub> is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. R<sub>θ,JC</sub> is guaranteed by design while R<sub>θCA</sub> is determined by the user's board design.
 b) R<sub>θ,JA</sub> = 96°C/W when mounted on a



a)  $R_{\theta JA} = 40^{\circ} C/W$  when mounted on a 1in<sup>2</sup> pad of 2 oz copper



b)  $R_{\theta JA} = 96^{\circ} C/W$  when mounted on a minimum pad.

Scale 1:1 on letter size paper

- 2. Pulse Test: Pulse Width  $< 300 \mu s$ , Duty Cycle < 2.0%
- 3. Maximum current is calculated as:  $\sqrt{R_{DS(ON)}}$
- 4. Starting  $T_J = 25^{\circ}C$ , L = TBD,  $I_{AS} = -6.7A$

## **Typical Characteristics**

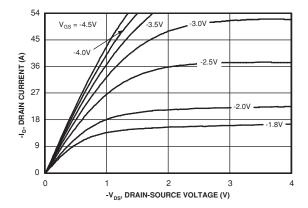


Figure 1. On-Region Characteristics.

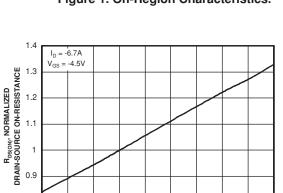


Figure 3. On-Resistance Variation withTemperature.

T<sub>.,</sub>, JUNCTION TEMPERATURE (°C)

75 100

150

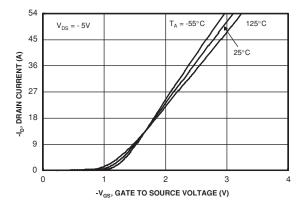


Figure 5. Transfer Characteristics.

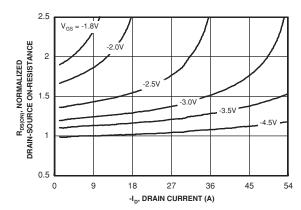


Figure 2. On-Resistance Variation with Drain Current and Gate Voltage.

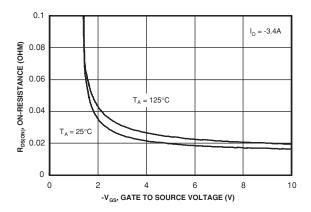


Figure 4. On-Resistance Variation with Gate-to-Source Voltage.

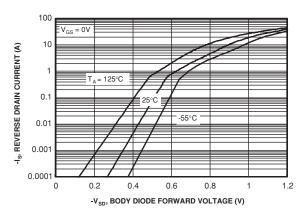
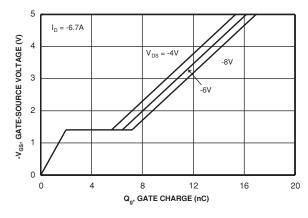


Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature.

0.8

-50

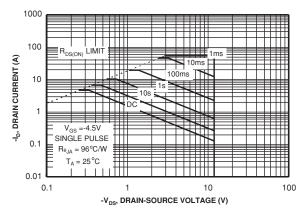
### **Typical Characteristics**



 $\begin{array}{c} 2400 \\ 2000 \\ \hline \\ 1600 \\ \hline \\ 400 \\ \hline \\ 0 \\ \hline \\ 3 \\ 6 \\ 9 \\ 12 \\ \hline \\ C_{rss} \\ \hline \\ C_{oss} \\ \hline \\ C_{oss} \\ \hline \\ C_{rss} \\ \hline \\ C_{rss} \\ \hline \\ C_{oss} \\ \hline \\ C_{rss} \\ C_{rss} \\ \hline \\ C_{rss} \\ C$ 

Figure 7. Gate Charge Characteristics.

Figure 8. Capacitance Characteristics.



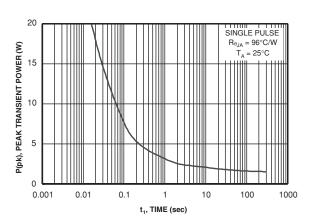


Figure 9. Maximum Safe Operating Area.

Figure 10. Single Pulse Maximum Power Dissipation.

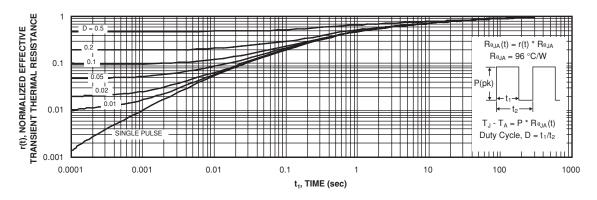


Figure 11. Transient Thermal Response Curve.

Thermal characterization performed using the conditions described in Note 1c. Transient thermal response will change depending on the circuit board design.

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Programmable Active Droop™		PACMAN™	SMART START™	VCX <sup>TM</sup>	

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